Application No.: 09/709,800   Docket No.: JCLA6349

In The Abstract:

Please amend the Abstract as follows:

A2

--An apparatus and a method of data processing system that uses multiply-accumulate instructions. The apparatus for processing data includes, ~~the apparatus comprising~~ a special register bank of N-bit data processing registers, a general register bank of N-bit data processing registers, a selector, a multiplier and an accumulator. The selector is coupled to the special register bank and the general register bank and is used for selecting one of the special and general register banks and outputting N-bit data from the selected register banks. The outputted N-bit data and the N-bit data held in the general register bank form a 2N-bit addition operand. The multiplier is used for performing multiply operation upon a first operand and a second operand and outputting an 2N-bit result. The accumulator is coupled to the multiplier, the selector and the general register bank and is used for performing accumulate operation upon the 2N-bit result and the 2N-bit addition operand and outputting a 2N-bit accumulated result. --

In The Drawings:

Please amend Figure 1 as shown in the attached amended drawing. The label "multiplien" of the reference number "30" is changed into "multiplier".